TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (L^2 - π -MOSV)

2SK2266

Chopper Regulator, DC–DC Converter and Motor Drive Applications

• 4-V gate drive

• Low drain-source ON resistance : $R_{DS(ON)} = 22 \text{ m}\Omega \text{ (typ.)}$

• High forward transfer admittance : $|Y_{fs}| = 27 \text{ S (typ.)}$

• Low leakage current : I_{DSS} = 100 μA (max) (V_{DS} = 60 V)

• Enhancement mode : $V_{th} = 0.8$ to 2.0 V ($V_{DS} = 10$ V, $I_D = 1$ mA)

Absolute Maximum Ratings (Ta = 25°C)

Characteristics		Symbol	Rating	Unit
Drain-source voltage		V_{DSS}	60	$(\checkmark\checkmark))$
Drain-gate voltage (R _{GS} = 20 kΩ)		V_{DGR}	60	A
Gate-source voltage		V_{GSS}	±20	V
Drain current	DC (Note 1)	ΙD	45	A
	Pulse (Note 1)	I_{DP}	180	
Drain power dissipatio	n (Tc = 25°C)	PD	65	W
Single pulse avalanche energy (Note 2)		EAS	246	(mJ
Avalanche current		IAR	45	A
Repetitive avalanche energy (Note 3)		EAR	6.5	mJ
Channel temperature		((T _{ch}))	150	//°C
Storage temperature ra	ange	T _{stg}	-55 to 150	,¢Ç

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Thermal Characteristics

Characteristics	Symbol	Max	Unit
Thermal resistance, channel to case	R _{th (ch-c)}	1.92	°C/W
Thermal resistance, channel to ambient	R _{th (ch-a)}	83.3	°C/W

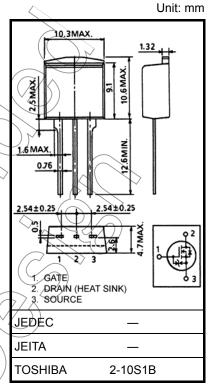
Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2: V_{DD} = 90 V, T_{ch} = 25°C (initial), L = 165 μ H, RG = 25 Ω , IAR = 45 A

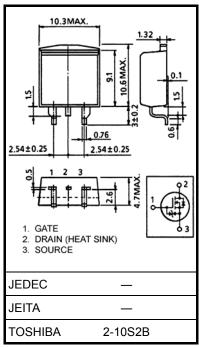
Note 3: Repetitive rating: pulse width limited by maximum channel temperature

This transistor is an electrostatic-sensitive device.

Please handle with caution.



Weight: 1.5 g (typ.)



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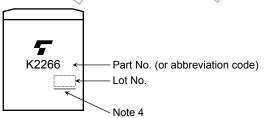
Electrical Characteristics (Ta = 25°C)

Charac	cteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cu	ırrent	I _{GSS}	V _{GS} = ±16 V, V _{DS} = 0 V	_	_	±10	μΑ
Drain cut-off cur	rrent	I _{DSS}	V _{DS} = 60 V, V _{GS} = 0 V	_	_	100	μA
Drain–source br voltage	eakdown	V (BR) DSS	I _D = 10 mA, V _{GS} = 0 V	60	_	_	V
Gate threshold v	oltage/	V _{th}	V _{DS} = 10 V, I _D = 1 mA	(0.8)	7	2.0	V
Drain-source ON resistance		D== (===	V _{GS} = 4 V, I _D = 15 A		40	55	m0
		R _{DS} (ON)	V _{GS} = 10 V, I _D = 25 A	22 30		30	mΩ
Forward transfer	r admittance	Y _{fs}	V _{DS} = 10 V, I _D = 25 A	_1/5	27	_	S
Input capacitano	e	C _{iss}		· —	1800	_	
Reverse transfer capacitance		C _{rss}	V _{DS} = 10 V, V _{GS} = 0 V, f = 1 MHz	_	350	_	pF
Output capacitance		Coss	$\langle \langle \rangle \rangle$	_	900	/>	
Switching time	Rise time	t _r	$V_{GS} \stackrel{10V}{\underset{0V}{\text{ID}}} \stackrel{1}{\underset{0}{\text{V}}} \stackrel{25\text{A}}{\underset{0}{\text{V}}} \stackrel{20}{\underset{0}{\text{V}}} \stackrel{20}{\underset{0}{\text{V}$	-(-	20	> —	
	Turn-on time	t _{on}			30	_	
	Fall time	t _f		_	ns		
	Turn–off time	t _{off}	$V_{DD} = 30V$ $Duty \leq 1\%, t_{W} = 10 \mu s$) —	130	_	
Total gate charge (Gate–source plus gate–drain)		Qg		_	60		
Gate–source charge		Q _{gs}	$V_{DD} \approx 48 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 45 \text{ A}$	_	40	_	nC
Gate-drain ("mil	ler") charge	Qgd		_	20	_	

Source-Drain Ratings and Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	I _{DR}	_	ı	1	45	Α
Pulse drain reverse current (Note 1)	I _{DRP}	_	_	_	180	Α
Forward voltage (diøde)	V _{DSF}	I _{DR} = 45 Å, V _{GS} = 0 V	1	-	-1.8	V
Reverse recovery time	t _{rr}	l _{DR} = 45 A, V _{GS} = 0 V, dl _{DR} / dt = 50 A / μs	1	90	1	ns
Reverse recovery charge	Qrr	1DR - 43 A, VGS - 0 V, diDR / dt - 30 A / μs		0.1		μC





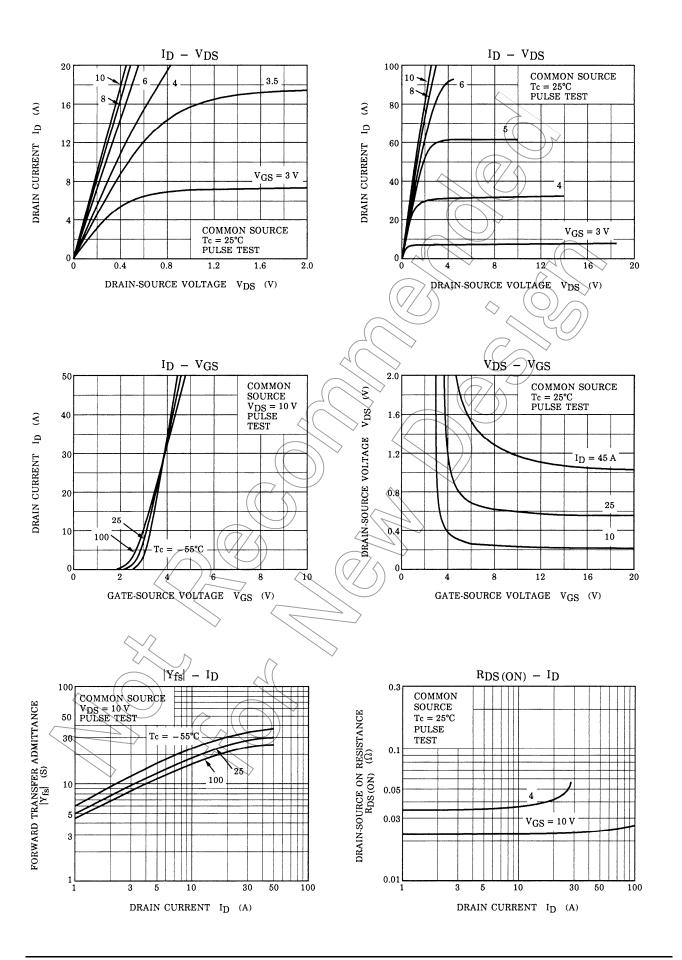
Note 4: A line under a Lot No. identifies the indication of product Labels.

Not underlined: [[Pb]]/INCLUDES > MCV

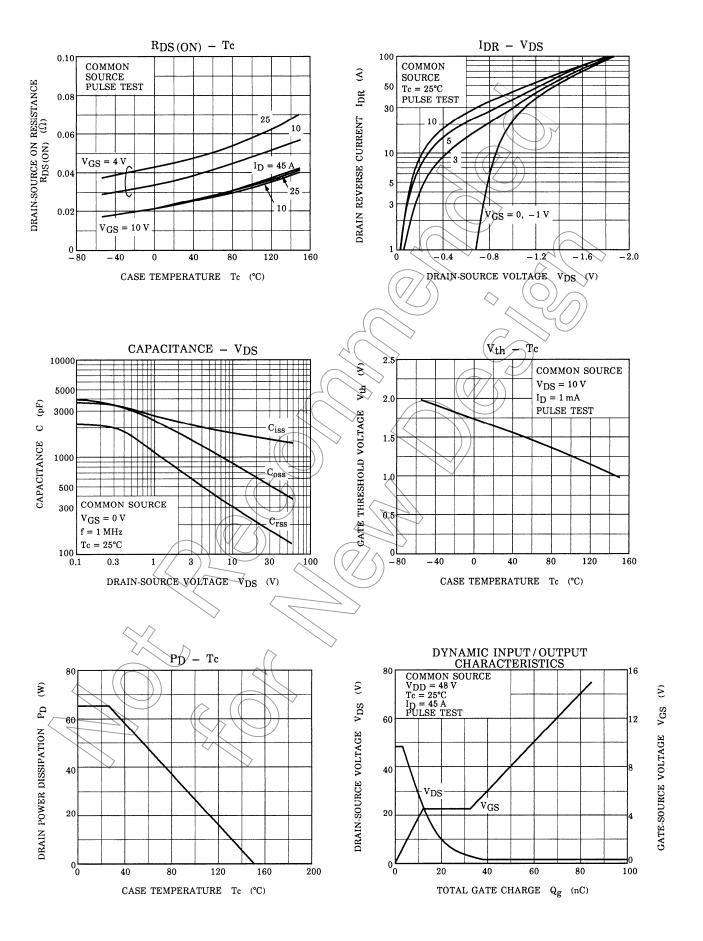
Underlined: [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

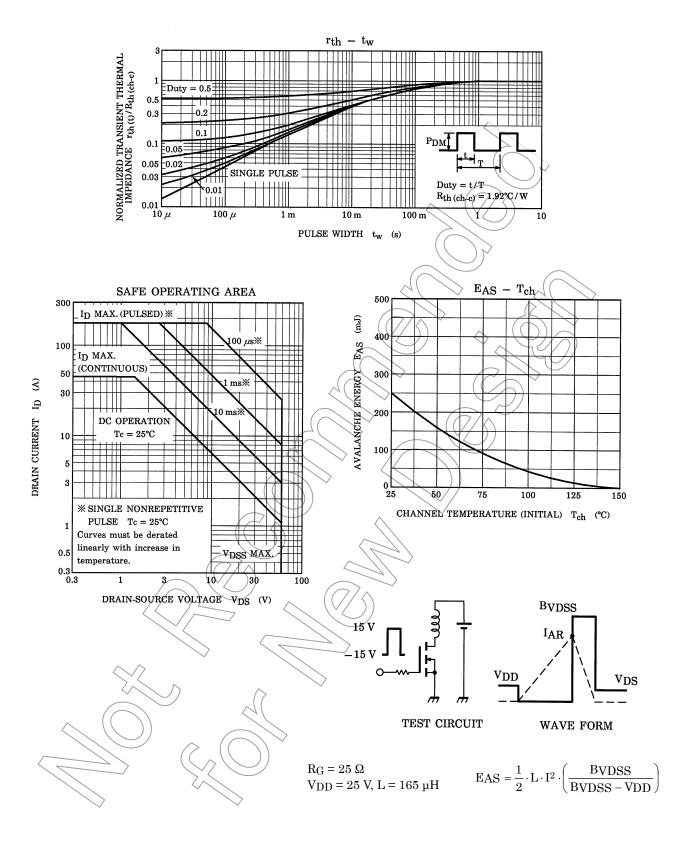
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