

KSC2518

High Speed, High Voltage Switching

- Low Collector Saturation Voltage
- Specified of Reverse Biased SOA With Inductive Load



1.Base 2.Collector 3.Emitter

NPN Epitaxial Silicon Transistor

Absolute Maximum Ratings $T_C=25$ °C unless otherwise noted

Symbol	Parameter	Value	Units
V_{CBO}	Collector-Base Voltage	500	V
V_{CEO}	Collector-Emitter Voltage	400	V
V _{EBO}	Emitter-Base Voltage	7	V
I _C	Collector Current (DC)	4	Α
I _{CP}	*Collector Current (Pulse)	8	Α
I _B	Base Current (DC)	1	Α
P _C	Collector Dissipation (T _C =25°C)	40	W
TJ	Junction Temperature	150	°C
T _{STG}	Storage Temperature	- 55 ~ 150	°C

^{*} PW≤350μs, Duty Cycle≤10%

Electrical Characteristics ${\rm T_{C}\text{=}}25^{\circ}{\rm C}$ unless otherwise noted

Symbol	Parameter	Test Condition	Min.	Max.	Units
V _{CEO} (sus)	Collector-Emitter Sustaining Voltage	$I_C = 2A$, $I_{B1} = 0.4A$, $L = 1mH$	400		V
V _{CEX} (sus)1	Collector-Emitter Sustaining Voltage	$I_C = 2A$, $I_{B1} = -I_{B2} = 0.4A$ $T_a = 125$ °C, $L = 180\mu$ H, Clamped	450		V
V _{CEX} (sus)2	Collector-Emitter Sustaining Voltage	$I_C = 4A$, $I_{B1} = 0.8A$, $-I_{B2} = 0.4A$ $T_a = 125$ °C, $L = 180\mu H$, Clamped	400		V
I _{CBO}	Collector Cut-off Current	$V_{CB} = 400V, I_{E} = 0$		10	μΑ
I _{CER}	Collector Cut-off Current	$V_{CE} = 400V, R_{BE} = 51\Omega @ T_{C} = 125^{\circ}C$		1	mA
I _{CEX1}	Collector Cut-off Current	$V_{CE} = 400V, V_{BE}(off) = -1.5V$ $V_{CE} = 400V, V_{BE}(off) = -1.5V$ @ $T_{C} = 125^{\circ}C$		10	μA mA
I _{EBO}	Emitter Cut-off Current	$V_{EB} = 5V, I_{C} = 0$		10	μΑ
h _{FE1} h _{FE2}	* DC Current Gain	$V_{CE} = 5V, I_{C} = 0.3A$ $V_{CE} = 5V, I_{C} = 1.5A$	20 10	80	
V _{CE} (sat)	* Collector-Emitter Saturation Voltage	$I_C = 1.5A, I_B = 0.3A$		1	V
V _{BE} (sat)	* Base-Emitter Saturation Voltage	$I_C = 1.5A, I_B = 0.3A$		1.5	V
t _{ON}	Turn ON Time	V _{CC} = 150V, I _C = 2A		1	μs
t _{STG}	Storage Time	$I_{B1} = -I_{B2} = 0.4A$		2.5	μs
t _F	Fall Time	$R_L = 75\Omega$		0.7	μs

^{*} Pulse Test: PW≤350μs, Duty Cycle≤2% Pulsed

h_{FE} Classification

Classification	R	0	Y
h _{FE1}	20 ~ 40	30 ~ 60	40 ~ 80

Typical Characteristics

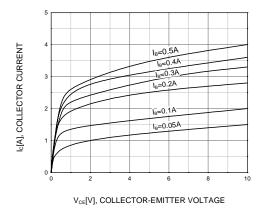


Figure 1. Static Characteristic

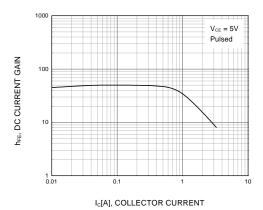


Figure 2. DC current Gain

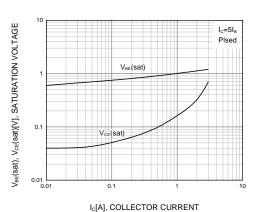


Figure 3. Base-Emitter Saturation Voltage Collector-Emitter Saturation Voltage

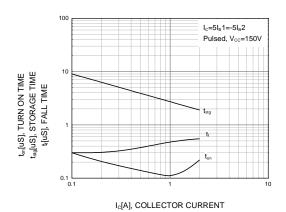


Figure 4. Turn On, Storage and Fall Time vs Collector Current

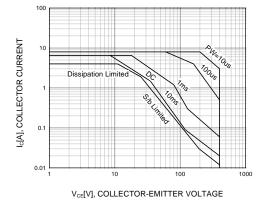
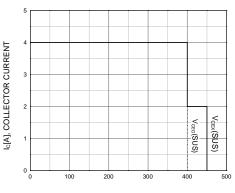


Figure 5. Forward Bias Safe Operating Area



 $V_{\text{CE}}(s)$, COLLECTOR-EMITTER VOLTAGE

Figure 6. Reverse Bias Safe Operating Area

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Typical Characteristics (Continued)

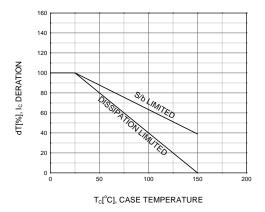


Figure 7. Derating Curve of Safe Operating Areas

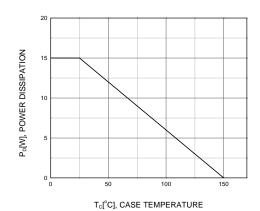
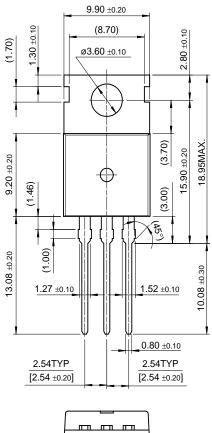
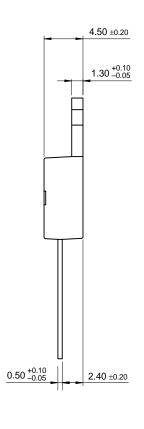


Figure 8. Power Derating

Package Demensions

TO-220





10.00 ±0.20

Dimensions in Millimeters

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