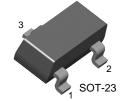


## KST92/93

## **High Voltage Transistor**



# **PNP Epitaxial Silicon Transistor**

## 1. Base 2. Emitter 3. Collector

## **Absolute Maximum Ratings** T<sub>a</sub>=25°C unless otherwise noted

Symbol	Parameter	Value	Units
$V_{CBO}$	Collector Base Voltage		
	: KST92	-300	V
	: KST93	-200	V
$V_{CEO}$	Collector-Emitter Voltage		
	: KST92	-300	V
	: KST93	-200	V
$V_{EBO}$	Emitter-Base Voltage	-5	V
I <sub>C</sub>	Collector Current	-500	mA
P <sub>C</sub>	Collector Power Dissipation	350	mW
T <sub>STG</sub>	Storage Temperature	150	°C
R <sub>TH</sub> (j-a)	Thermal Resistance junction to Ambient	357	°C/W

Refer to KSP92/93 for graphs

## $\textbf{Electrical Characteristics} \ \, \textbf{T}_{a} \!\!=\!\! 25^{\circ} \textbf{C} \ \, \textbf{unless otherwise noted}$

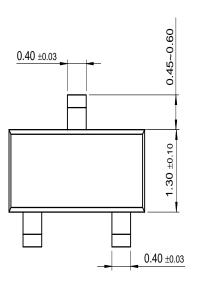
Symbol	Parameter	Test Condition	Min.	Max.	Units
BV <sub>CBO</sub>	Collector-Base Breakdown Voltage : KST92	I <sub>C</sub> = -100μA, I <sub>E</sub> =0	-300		V
	: KST92		-200		V
BV <sub>CEO</sub>	* Collector-Emitter Breakdown Voltage	I <sub>C</sub> = -1mA, I <sub>B</sub> =0			
	: KST92 : KST93		-300 -200		V
BV <sub>EBO</sub>	Emitter-Base Breakdown Voltage	I <sub>E</sub> = -100μA, I <sub>C</sub> =0	-5		V
I <sub>CBO</sub>	Collector Cut-off Current : KST92 : KST93	V <sub>CB</sub> = -200V, I <sub>E</sub> =0 V <sub>CB</sub> = -160V, I <sub>E</sub> =0		-0.25 -0.25	μ <b>Α</b> μ <b>Α</b>
I <sub>EBO</sub>	Emitter Cut-off Current	V <sub>EB</sub> = -5V, I <sub>C</sub> =0		-0.1	μΑ
h <sub>FE</sub>	* DC Current Gain	V <sub>CE</sub> = -10V, I <sub>C</sub> = -1mA V <sub>CE</sub> = -10V, I <sub>C</sub> = -10mA V <sub>CE</sub> = -10V, I <sub>C</sub> = -30mA	25 40 25		
V <sub>CE</sub> (sat)	* Collector-Emitter Saturation Voltage	I <sub>C</sub> = -20mA, I <sub>B</sub> = -2mA		-0.5	V
V <sub>BE</sub> (sat)	* Base-Emitter Saturation Voltage	$I_C$ = -20mA, $I_B$ = -2mA		-0.9	V
C <sub>ob</sub>	Output Capacitance : KST92 : KST93	V <sub>CB</sub> = -20V, I <sub>E</sub> =0 f=1MHz		6 8	pF pF
f <sub>T</sub>	Current Gain Bandwidth Product	V <sub>CE</sub> = -20V, I <sub>C</sub> = -10mA f=100MHz	50		MHz
* Pulse Test: PW	/≤300μs, Duty Cycle≤2%				

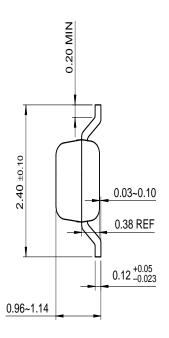
# Marking Code Type KST92 KST93 Mark 2D 2E

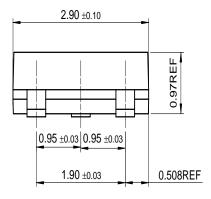


# **Package Dimensions**

# **SOT-23**







Dimensions in Millimeters

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E <sup>2</sup> CMOS™	HiSeC™	MSXPro™	Quiet Series™	TruTranslation™
EnSigna™	I <sup>2</sup> C <sup>TM</sup>	$OCX^{TM}$	RapidConfigure™	UHC™ _
Across the board.	Around the world.™	OCXPro™	RapidConnect™	UltraFET <sup>®</sup>
The Power Franchise™		OPTOLOGIC <sup>®</sup>	SILENT SWITCHER®	VCX <sup>TM</sup>
Programmable Ad	ctive Droop™	OPTOPLANAR™	SMART START™	

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