

2N6518

High Voltage Transistor

- Collector-Emitter Voltage: V_{CEO}= -250V
 Collector Dissipation: P_C (max)=625mW
 Complement to 2N6515



PNP Epitaxial Silicon Transistor

1. Emitter 2. Base 3. Collector

Absolute Maximum Ratings T_a=25°C unless otherwise noted

Symbol	Parameter	Value	Units
V_{CBO}	Collector-Base Voltage	-250	V
V _{CEO}	Collector-Emitter Voltage	-250	V
V _{EBO}	Emitter-Base Voltage	-5	V
I _C	Collector Current	-500	mA
I _B	Base Current	-250	mA
P _C	Collector Power Dissipation	625	mW
	Derate above 25°C	5	mW/°C
T _J	Junction Temperature	150	°C
T _{STG}	Storage Temperature	-55 ~ 150	°C

Refer to 2N6520 for graphs

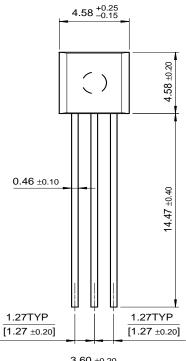
Electrical Characteristics T_a =25°C unless otherwise noted

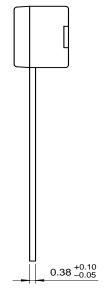
Symbol	Parameter	Test Condition	Min.	Max.	Units
BV _{CBO}	* Collector-Base Breakdown Voltage	I _C = -100μA, I _E =0	-250		V
BV _{CEO}	Collector-Emitter Breakdown Voltage	I _C = -1mA, I _B =0	-250		V
BV _{EBO}	Emitter-Base Breakdown Voltage	I _E = -10μA, I _C =0	-5		V
I _{CBO}	Collector Cut-off Current	V _{CB} = -150V, I _E =0		-50	nA
I _{EBO}	Emitter Cut-off Current	V _{EB} = -4V, I _C =0		-50	nA
h _{FE}	* DC Current Gain	V _{CE} = -10V, I _C = -1mA V _{CE} = -10V, I _C = -10mA V _{CE} = -10V, I _C = -30mA V _{CE} = -10V, I _C = -50mA V _{CE} = -10V, I _C = -100mA	35 50 50 45 25	300 220	
V _{CE} (sat)	Collector-Emitter Saturation Voltage	I _C = -10mA, I _B = -1mA I _C = -20mA, I _B = -2mA I _C = -30mA, I _B = -3mA I _C = -50mA, I _B = -5mA		-0.30 -0.35 -0.50 -1	V V V
V _{BE} (sat)	Base-Emitter Saturation Voltage	I _C = -10mA, I _B = -1mA I _C = -20mA, I _B = -2mA I _C = -30mA, I _B = -3mA		-0.75 -0.85 -0.90	V V V
V _{BE} (on)	Base-Emitter On Voltage	V _{CE} = -10V, I _C = -100mA		-2	V
f _T	* Current Gain Bandwidth Product	V _{CE} = -20V, I _C = -10mA, f=20MHz	40	200	MHz
C _{ob}	Output Capacitance	V _{CB} = -20V, I _E =0, f=1MHz		6	pF
C _{EB}	Emitter-Base Capacitance	V _{EB} = -0.5V, I _C =0, f=1MHz		100	pF
t _{ON}	Turn On Time	V_{BE} (off)= -2V, V_{CC} = -100V I_{C} = -50mA, I_{B1} = -10mA		200	ns
t _{OFF}	Turn Off Time	V _{CC} = -100V, I _C = -50mA I _{B1} =I _{B2} =10mA		3.5	ns

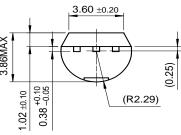
^{*} Pulse Test: Pulse Width≤300μs, Duty Cycle≤2%

Package Dimensions

TO-92







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