Vishay Semiconductors

Three Phase Inverter Module in MTP Package 1200 V NPT IGBT and HEXFRED® Diodes, 15 A



МТР

PRODUCT SUMMARY					
V _{CES}	1200 V				
$V_{CE(on)}$ typical at V_{GE} = 15 V	2.51 V				
I_C at T_C = 100 °C	15 A				
t _{sc} at T _J = 150 °C	> 10 µs				

FEATURES

- Generation 5 NPT 1200 V IGBT technology
- HEXFRED[®] diode with ultrasoft reverse recovery
- Very low conduction and switching losses
- Optional SMT thermistor (NTC)
- Aluminum oxide DBC
- Very low stray inductance design for high speed operation
- Short circuit 10 µs
- Square RBSOA
- Operating frequencies 8 kHz to 60 kHz
- UL approved file E78996 😱
- Compliant to RoHS directive 2002/95/EC
- Designed and qualified for industrial level

BENEFITS

- · Optimized for inverter motor drive applications
- Low EMI, requires less snubbing
- Direct mounting to heatsink
- PCB solderable terminals
- Very low junction to case thermal resistance

ABSOLUTE MAXIMUM RATINGS				
PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS
Collector to emitter voltage	V _{CES}		1200	V
		T _C = 25 °C	30	
Continuous collector current	Ι _C	T _C = 100 °C	15	
Pulsed collector current	I _{CM}		60	А
Peak switching current	I _{LM}		60	A
Diode continuous forward current	١ _F	T _C = 100 °C	15	
Peak diode forward current	I _{FM}		30	
Gate to emitter voltage	V _{GE}		± 20	N/
RMS isolation voltage	V _{ISOL}	Any terminal to case, t = 1 min	2500	V
Maximum power dissipation	D	T _C = 25 °C	187	10/
(including diode and IGBT)	P _D	T _C = 100 °C	75	W





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ELECTRICAL SPECIFICATIONS (T _J = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS	
Collector to emitter breakdown voltage	V _{(BR)CES}	$V_{GE} = 0 \text{ V}, I_{C} = 250 \ \mu\text{A}$	1200	-	-	V	
Temperature coefficient of V(BR)CES	$\Delta V_{(BR)CES} / \Delta T_J$	V _{GE} = 0 V, I _C = 1 mA	-	1.11	-	V/°C	
		V _{GE} = 15 V, I _C = 15 A	-	2.51	2.70		
Collector to emitter voltage	V	V _{GE} = 15 V, I _C = 30 A	-	3.36	3.66		
Collector to entitler voltage	V _{CE(on)}	$V_{GE} = 15 \text{ V}, \text{ I}_{C} = 15 \text{ A}, \text{ T}_{J} = 125 ^{\circ}\text{C}$	-	2.94	3.16	V	
		V_{GE} = 15 V, I_{C} = 30 A, T_{J} = 125 °C	-	4.12	4.46		
Gate threshold voltage	V _{GE(th)}	I _C = 250 μA	4	-	6		
Temperature coefficient of threshold voltage	$\Delta V_{GE(th)} / \Delta T_J$	$V_{CE} = V_{GE}$, $I_C = 1 \text{ mA}$	-	- 10	-	mV/°C	
Forward transconductance	9 _{fe}	V _{CE} = 25 V, I _C = 15 A	-	12	-	S	
	I	$V_{GE} = 0 \text{ V}, \text{ V}_{CE} = 1200 \text{ V}$	-	-	250		
Collector to emitter leaking current	ICES	V_{GE} = 0 V, V_{CE} = 1200 V, T_{J} = 125 °C	-	-	1000	- μΑ	
	V _{FM}	I _F = 15 A, V _{GE} = 0 V	-	2.13	2.58	v	
Diada famuard valtaga drag		I _F = 30 A, V _{GE} = 0 V	-	2.70	3.33		
Diode forward voltage drop		$I_F = 15 \text{ A}, V_{GE} = 0 \text{ V}, T_J = 125 \text{ °C}$	-	2.27	2.75		
		$I_F = 30 \text{ A}, V_{GE} = 0 \text{ V}, T_J = 125 \text{ °C}$	-	3.06	3.76		
Gate to emitter leakage current	I _{GES}	$V_{GE} = \pm 20 \text{ V}$	-	-	± 250	nA	

PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS	
Total gate charge (turn-on)	Qg	I _C = 15 A		98	146		
Gate to emitter charge (turn-on)	Q _{ge}	V _{CC} = 600 V	-	12	17	nC	
Gate to collector charge (turn-on)	Q _{gc}	V _{GE} = 15 V	-	46	69		
Turn-on switching loss	E _{on}	$I_{C} = 15 \text{ A}, V_{CC} = 600 \text{ V}, V_{GE} = 15 \text{ V}$	-	0.990	1.485		
Turn-off switching loss	E _{off}	$R_g = 10 \Omega$, L = 500 μH, T _J = 25 °C Energy losses include tail and	-	0.827	1.241	mJ	
Total switching loss	E _{ts}	diode reverse recovery	-	1.817	2.726		
Turn-on switching loss	E _{on}	$I_{C} = 15 \text{ A}, V_{CC} = 600 \text{ V}, V_{GE} = 15 \text{ V}$	-	1.352	2.028		
Turn-off switching loss	E _{off}	$R_g = 10 \Omega$, L = 500 μH, $T_J = 125 °C$ Energy losses include tail and	-	1.138	1.707	mJ	
Total switching loss	E _{ts}	diode reverse recovery	-	2.490	3.735		
Turn-on delay time	t _{d(on)}		-	95	143	ns	
Rise time	tr	I _C = 15 A, V _{CC} = 600 V, V _{GE} = 15 V L = 500 μH, L _S = 100 nH	-	18	27		
Turn-off delay time	t _{d(off)}	$R_{a} = 10 \Omega, T_{J} = 125 °C$	-	134	200		
Fall time	t _f	_ · ·g · ·, · · · · · · ·	-	227	341		
Reverse BIAS safe operating area	RBSOA	$T_J = 150$ °C, $I_C = 60$ A $R_g = 10$ Ω, $V_{GE} = 15$ V to 0	Fullsquare				
Short circuit safe operating area	SCSOA	$V_{CC} = 600 \text{ V}, V_{GE} = + 15 \text{ V to } 0$ $T_{J} = 150 \text{ °C}, V_{P} = 1200 \text{ V}, R_{g} = 10 \Omega$	10	-	-	μs	
Input capacitance	C _{ies}	V _{GE} = 0 V	-	1302	1953		
Output capacitance	C _{oes}	$V_{CC} = 30 V$	-	717	1076	pF	
Reverse transfer capacitance	C _{res}	f = 1 MHz	-	38	57		
Diode reverse recovery energy	E _{rec}	I _C = 15 A, V _{CC} = 600 V, V _{GE} = 15 V	-	819	-	μJ	
Diode reverse recovery time	t _{rr}	$L = 500 \ \mu H, \ L_S = 100 \ n H$	-	96	-	ns	
Diode peak reverse current	I _{rr}	R _g = 10 Ω, T _J = 125 °C	-	35	-	А	



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THERMISTOR SPECIFICATIONS (T CODE ONLY)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Resistance	R ₀ ⁽¹⁾	T ₀ = 25 °C	-	30	-	kΩ
Sensitivity index of the thermistor material	β (1)(2)	T ₀ = 25 °C T ₁ = 85 °C	-	4000	-	К

Notes

 $^{(1)}\ T_0,\,T_1$ are thermistor's temperatures

⁽²⁾
$$\frac{R_0}{R_1} = \exp\left[\beta\left(\frac{1}{T_0} - \frac{1}{T_1}\right)\right]$$

THERMAL AND MECHANICAL SPECIFICATIONS							
PARAMETER		SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Operating junction temperature range		TJ		- 40	-	150	°C
Storage temperature	range	T _{Stg}		- 40	-	125	
	IGBT			-	-	1.1	
Junction to case	Diode	R _{thJC}		-	-	1.7	°C/W
	Module			-	0.50	-	0/10
Case to sink per mod	lule	R _{thCS}	Heatsink compound thermal conductivity = 1 W/mK	-	0.1	-	
Mounting torque				-	-	4	Nm
Weight				-	65	-	g





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Fig. 6 - Typical Switching Time vs. I_C T_J = 125 °C, L = 500 μ H, V_{CE} = 600 V R_g = 10 Ω ; V_{GE} = 15 V



Fig. 7 - Typical Energy Loss vs. R_g T_J = 125 °C, L = 500 $\mu H,$ V_{CE} = 600 V I_C = 15 A; V_{GE} = 15 V



 $I_{C} = 15 \text{ A}; V_{GE} = 15 \text{ V}$

For technical questions within your region, please contact one of the following: DiodesAmericas@vishay.com, DiodesAsia@vishay.com, DiodesEurope@vishay.com



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Fig. 12 - Power Dissipation vs. Case Temperature (IGBT only)





Fig. 14 - Reverse BIAS SOA T_{J} = 150 °C, V_{GE} = 15 V

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Fig. 20 - Maximum Transient Thermal Impedance, Junction to Case (IGBT)



Fig. 21 - Maximum Transient Thermal Impedance, Junction to Case (Diode)

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ORDERING INFORMATION TABLE



CIRCUIT CONFIGURATION



LINKS TO RELATED DOCUMENTS				
Dimensions	www.vishay.com/doc?95175			

Outline Dimensions

Vishay Semiconductors



MTP

DIMENSIONS in millimeters



Note

• Unused terminals are not assembled in the package



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