

## **Multilayer Ceramic Chip Capacitor**

## Part Number: 1206Y2K00222KET

1206 2000V 2.2nF ±10% X7R (2R1) to AEC **Description:** -Q200

A range of X7R MLC capacitors fully tested and approved to automotive specification AEC-Q200. Available in 0603 to 3640 chip sizes with capacitance values up to 5.6uF as standard. A variety of termination options are available, including FlexiCap<sup>™</sup>, the world's first commercially available flexible termination.

Parts with WS2 suffix use StackiCap<sup>™</sup> technology. Parts with NC suffix code must be conformally coated after mounting (including between board and chip) to prevent flashover.



| Mechanical Specification   |  |   |   |  |  |
|--|--|---|---|--|--|
| Size Code  |  | 1206  |   |  |  |
| Length (L1) in mm (")  |  | 3.2 +0.30/-0.20 (0.126 +0.012/-0.008)   |   |  |  |
| Width (W) in mm (")  |  | 1.6 ± 0.20 (0.063 ± 0.008)  |   |  |  |
| Thickness (T) in mm (")  |  | 1.7 Max (0.068 Max)   |   |  |  |
| Minimum Termination Band (L2,L3) in mm (")   |  | 0.25 (0.010)  |   |  |  |
| Maximum Termination Band (L2,L3) in mm (")   |  | 0.75 (0.030)  |   |  |  |
| Termination Material   |  | FlexiCap™ Polymer termination, Nickel barrier, Sn Plated Solder<br>(RoHS compliant) |   |  |  |
| Solderability  |  | IEC-60068-2-58  |   |  |  |
| Packaging  |  | 7" Reel Horizontal C  | orientation, 2500 per reel  |  |  |
|  |  |   |   |  |  |
|  |  |   |   |  |  |
| General Electrical Specification   |  |   |   |  |  |
| Rated Voltage  |  | 2000Vdc   |   |  |  |
| Nominal Capacitance Value  |  | 2.2nF   |   |  |  |
| Capacitance Tolerance  |  | ±10%  |   |  |  |
| Tangent of Loss Angle (Tan $\delta$ )  |  | ≤0.025  |   |  |  |
| Capacitance and Tan $\delta$ Test Conditions   |  | 1.0Vrms @ 1kHz  |   |  |  |
| Voltage Proof  |  | 2400Vdc   |   |  |  |
| (Voltage applied for 5 secs max. @ 50mA max. charge current)                             |  |   |   |  |  |
| Min Insulation Resistance (IR)   |  | 100.00GOhm @ 100Vdc   |   |  |  |
| Dielectric Classification  |  | X7R (2R1) to AEC-Q200   |   |  |  |
| Rated Temperature Range  |  | -55°C / +125°C  |   |  |  |
| Maximum Capacitance Change over Temperature Range  |  | No DC Voltage ±15%  |   |  |  |
|  |  | Rated DC Voltage -  |   |  |  |
| Climatic Category (IEC)  |  | 55/125/56   |   |  |  |
| Ageing Characteristic  |  | <2% per decade  |   |  |  |
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## Multilayer Ceramic Chip Capacitor

| Part Number: 1206Y2K00222K  | ET  | Description:  | 1206 2000V 2.2nF ±10% X7R (2R1) to AEC<br>-Q200   |  |  |
|---|---|---|---|--|--|
| Environmental   |   |   |   |  |  |
| RoHS Compliant to 2011/65/EC as amended by 2015/863/EU Compliant  |   |   |   |  |  |
| REACH Compliant   |   | 191 compliant   |   |  |  |
| California Proposition 65   |   | No exposure risk  |   |  |  |
| Board Layout  |   |   |   |  |  |
|   | Duaru   | Layout  |   |  |  |
| Knowles' conventional 2-terminal chip capacitors can<br>generally be mounted using pad designs in accordance with<br>international specification IPC-7351, Generic Requirements<br>for Surface Mount Design and Land Pattern Standards, but<br>there are some other factors that have been shown to reduce<br>mechanical stress, such as reducing the pad width to less<br>than the chip width. In addition, the position of the chip on the<br>board should be considered.                                 |   | Ē   | PC-7351 pad design<br>1206<br>C 3.00mm 0.118"<br>Y 1.15mm 0.045"<br>X 1.80mm 0.071"<br>- Y                                      |  |  |
| Some high voltage parts may require more<br>board layout and/or the addition of a cont<br>prevent flashover. Refer to application n<br>further information.   | formal coating to   |   | x 1 c   |  |  |
| Packaging   |   |   |   |  |  |
| Tape packaging information for tape-and-reel parts:<br>Tape and reel packing of surface mounting chip capacitors for<br>automatic placement are in accordance with IEC60286-3.  |   |   | Product identifying label<br>Plastic carrier tape<br>Top<br>tape<br>8 or 12mm<br>178mm (7") or<br>nominal 330mm (13") dia. reel |  |  |
| Soldering   |   |   |   |  |  |
| Reflow solder in accordance with IPC-A-610. Recommended<br>reflow profile as laid down in<br>IPC/JEDEC J-STD-020.<br>Wave soldering is also possible, but care must be taken for<br>case sizes 1210 and larger and component thickness<br>>1.0mm. Trials are encouraged.<br>Hand soldering is not recommended and can lead to<br>component damage through thermal shock.<br>PdAg terminations are primarily intended for conductive epoxy<br>attachment - they may be suitable for soldering but trials are |   |   |   |  |  |
| Application notes with mounting and handling guidance are available on request.   |   |   |   |  |  |
| Compex DLI Jo   | ohanson MFG   | Novacap   | Syfer Voltronics  |  |  |
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